Teză de doctorat

Titlu: Self-Healing and Secure Low-Power Memory Systems
Perioada: 2011 – 2017
Conducător ştiinţific: Prof. Dr. Ing. Salvador Manich, Prof. Dr. Ing. Joan Figueras
Domeniu: Inginerie Electronică
Instituţia: Universitatea Politehnică din Catalunya BarcelonaTECH, Departamentul de Inginerie Electronică, Barcelona, Spania

Titlu: Self-Healing For Low-Power Systems
Perioada: 2009 – 2012
Conducător ştiinţific: Prof. Dr. Ing. Liviu Miclea
Domeniu: Ingineria Sistemelor
Instituţia: Universitatea Tehnică din Cluj-Napoca, Facultatea de Automatică şi Calculatoare, Cluj-Napoca, România

Cărți

1. Oprișa, C., Hangan, A, **Neagu, M.**, Cebuc, E., Sebestyen, G., Programare în Limbaj de Asamblare, Îndrumător de laborator, UTPRESS, Cluj-Napoca, 2018, ISBN 978-606-737-333-2
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5. **Neagu, M.**, „Sustainable Smart Cities: A Fog Computing Framework for a Smart Urban Transport Network", Studia Universitatis "Vasile Goldis" Arad Economics Series, Vol. 28, No. 4, pp. 68 – 80, 2018, ISSN 1584 – 2339
6. **Neagu, M.,** Teodoru, M.C., "Testing the Engel's law in the consumption pattern of Romanian population", Studia Universitatis "Vasile Goldis" Arad Economics Series, Vol. 27, No. 3, pp., 33 – 53, 2017, ISSN 1584 – 2339
7. Neagu, O., **Neagu, M.**, „Regional Specialisation and Economic Concentration in Romania", Studia Universitatis "Vasile Goldis" Arad Economics Series, Vol. 26, No. 3, pp., 1 – 17, 2016, ISSN 1584 – 2339
8. **Neagu, M.**, Miclea, L., "On-Line Error Detecting Codes for DRAMs", ACAM Journal: Automation, Computers, Applied Mathematics, Vol. 20, Nr. 2, pp. 133 – 138, 2011, ISSN 1221-437X

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10. **Neagu, M.**; Miclea, L.; Manich, S.; „Defeating Simple Power Analysis Attacks in Cache Memories", in Proceedings of the 2015 30th Conference on Design of Circuits and Integrated Systems (DCIS), pp. 1 – 6, Estoril, Portugal, 2015
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12. **Neagu, M.**, Sebestyen, G., "Increasing Memory Security through Information Entropy Models", 15th IEEE International Symposium on Computational Intelligence and Informatics (CINTI), pp. 49-53, Budapest, Hungary, 2014
13. **Neagu, M.**, Miclea, L., "Protecting Cache Memories through Data Scrambling Technique", 10th International Conference on Intelligent Computer Communication and Processing (ICCP), pp. 297 – 303, Cluj-Napoca, Romania, 2014
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21. Neagu, O.; **Neagu, M.**; „Empirical Evidence on Wage Divergence in the Case of Romania", Proceedings of 2nd International Conference on Regional Sustainable Development – Through Competitiveness, Innovation and Human Capital, Satu Mare, Romania, 2015
22. Neagu, O., **Neagu, M.**; "Economic freedom and growth in Eastern Europe-the effect of foreign direct investment (FDI). A panel data estimation", Proceedings of 2nd International Conference on Recent Advances in Economic and Social Research, Bucharest, Romania, 2016
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25. **Neagu, M.**; „Challenges Of Internet Of Things In Developing Countries", Proceedings of 5th International Conference on Regional Sustainable Development – Through Competitiveness, Innovation and Human Capital, Satu Mare, Romania, 2018

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- **Neagu, M.**, Time performance and power efficiency of Interleaved Scrambling Technique for cache memories, ACAM Journal: Automation, Computers, Applied Mathematics, ISSN 1221-437X, acceptată şi în curs de publicare

Lucrări de laborator

1. **Neagu, M.,** Hângan, A., Sebestyen, G., Structure of Computer Systems, lucrare de laborator 03 – Programming elements in VHDL, format electronic, http://users.utcluj.ro/~madalin/structure-of-computer-systems.html
2. **Neagu, M.,** Hângan, A., Sebestyen, G., Structure of Computer Systems, lucrare de laborator 04 – Design of ALU components, format electronic, http://users.utcluj.ro/~madalin/structure-of-computer-systems.html
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4. Hângan, A., **Neagu, M.,** Sebestyen, G., Structure of Computer Systems, lucrare de laborator 10 – Memory design - Part 1, format electronic, http://users.utcluj.ro/~madalin/ structure-of-computer-systems.html

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6. Hângan, A., **Neagu, M.,** Sebestyen, G., Sisteme cu Microprocesoare, lucrare de laborator 1 – Structura unui calculator. Măsurarea performanţelor unui calculator, format electronic, http://users.utcluj.ro/~madalin/microprocessor-based-systems.html
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Participari în proiecte

1. TRAMS: Terascale Reliable Adaptive Memory Systems (Referinta: FP7-248789), proiecte internaţional, perioada 2010 – 2011, membru
2. Análisis y técnicas de mejora de la robustez y seguridad de circuitos nanométricos en presencia de ataques, defectos, variabilidad y Aging (TEC2013-J41209-P), proiect internaţional, perioada 2014 – 2017, membru

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