

LISTĂ COMPLETĂ DE LUCRĂRI

Emilia ȘIPOŞ

a) Lucrări cele mai relevante pentru realizările profesionale

1. **Şipoş, Emilia**, Ciuciu, C., Ivanciu, Laura, Sensor-Based Prototype of a Smart Assistant for Visually Impaired People—Preliminary Results" Sensors 22, no. 11: 4271, 2022 <https://doi.org/10.3390/s22114271>
2. Ivanciu, Laura-Nicoleta, **Şipoş, Emilia**, Learning by doing: challenges and achievements during a year of online teaching, 17th International Scientific Conference on eLearning and Software for Education, eLSE 2021, Bucharest, 22 - 23 April 2021, Pages 452 – 461, DOI: 10.12753/2066-026X-21-124
3. **Şipoş, Emilia**, Oneş, Alexandra, Groza, R., Failure detection on PCBs: an image processing based approach. In: 2019 IEEE 25th International Symposium for Design and Technology in Electronic Packaging (SIITME). IEEE, 2019. p. 375-378
4. Ivanciu, Laura-Nicoleta, **Şipoş, Emilia**, Hybrid System for Dropout Risk Evaluation of First Year Students, 2nd International Conference on Applied Mathematics, Modeling and Simulation (AMMS), Beijing, PEOPLES R CHINA, AUG 26-27, 2018, DEStech Transactions on Computer Science and Engineering, Vol. 305, pp. 74-78
5. **Şipoş, Emilia**, Ivanciu, Laura-Nicoleta, Failure Analysis and Prediction using Neural Networks in the Chip Manufacturing Process, 40th International Spring Seminar on Electronics Technology (ISSE), Sofia, BULGARIA, MAY 10-14, 2017
6. **Şipoş, Emilia**, Groza, R., Ivanciu, Laura, Power consumption and noise margin comparison between simple ternary inverter and binary inverter, Acta Technica Napocensis, ISSN 1221-6542, Vol.58, Iss2/2017, pp.13
7. **Şipoş, Emilia**, Cica, L., Miron, C. Software Minimization of Ternary Functions Using Matlab, IEEE International Conference on Automation, Quality and Test Robotics, AQTR 2012, Cluj-Napoca, Romania, 24-27 May, pp. 284-289
8. Oltean, G., Miron, C., **Şipoş, Emilia**, Ivanciu, Laura, A top-down approach to gaining an insight into a transistor circuit in CS connection, International Journal of Electrical Engineering Education (IJEEE), Vol. 48, No.1, Jan.2011, ISSN 0020-7209, pp. 10-16
9. **Şipoş, Emilia**, Miron C., Master-slave ternary D flip-flap-flops with triggered edges control, 2010 IEEE International Conference on Automation Quality and Testing Robotics, AQTR 2010, Cluj-Napoca, Romania, 28-30 May 2010, Vol. 2, pp. 149-155, ISBN 978-1-4244-6724-2
10. Miron, C., **Şipoş, Emilia**, The CMOS differential stage with active load intuitively demonstrates the unequal split of the differential input voltage , IEEE Transactions on Education, ISSN 0018-9359, Volume 49, Issue 4, Nov. 2006, pp. 481 – 488.

b) Teza de doctorat

1. Sinteză unor circuite electronice logice multivalente, UTCN, 2012
Îndrumător Prof.dr.ing. Costin MIRON

c) Brevete de invenție -

d) Cărți și capitole în cărți

1. **Şipoş, Emilia**, Ivanciu, Laura, Dispozitive Electronice, Cluj-Napoca, U.T.Press, ISBN 978-606-737-576-3, 2022
2. Ivanciu, Laura, **Şipoş, Emilia**, Electronic Devices – Laboratory manual, Cluj-Napoca, U.T.Press, ISBN 978-973-662-542-8, 2022, Online
3. **Şipoş, Emilia**, Ivanciu, Laura, Dispozitive electronice - Îndrumător de laborator, Cluj-Napoca, U.T.Press, ISBN 978-606-738-580-0, 2022, Online
4. **Şipos, Emilia**, Aplicații ale logicii trivalente în domeniul circuitelor electronice, ISBN 978-606-737-198-7, 96 pag, 2016
5. **Şipoş Emilia**, Ivanciu Laura, Dispozitive electronice. Probleme rezolvate, ISBN 978-606-737-191-8, 128 pag, 2016, Online
6. Oltean, G., **Şipoş, Emilia**, Miron, C., Ivanciu, Laura, Electronic Devices. Laboratory Manual Cluj-Napoca, U.T. Press, ISBN 978-973-662-542-8, 90 pag, 2010
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8. **Şipoş, Emilia**, Oltean, G., Miron, C., Ivanciu, Laura, Gordan, Mihaela, Fundamental Electronic Circuits. Laboratory Manual Cluj-Napoca, U.T. Press, ISBN 978-973-662-503-9, 91 pag, 2009
9. **Şipoş, Emilia**, Oltean, G., Miron, C., Ivanciu, Laura, Gordan, Mihaela, Circuite electronice fundamentale. Îndrumător de laborator, Cluj-Napoca, U.T. Press, ISBN 978-973-662-502-2, 97 pag, 2009
10. Oltean, G, **Şipoş, Emilia**, Tehnici fuzzy în proiectarea și modelarea circuitelor analogice, U.T. Pres, Cluj-Napoca, ISBN 978-973-662-302-8, 129 pag, 2007
11. Oltean, G., Gordan, Mihaela, Miron, C., **Şipoş, Emilia**, Crasi, M., Laboratory Manual for Electronic Devices and Circuits. Electronic Circuits, Editura U.T. Press, Cluj Napoca, ISBN 973-662-072-7, 104 pag, 2004
12. Oltean, G., Gordan, Mihaela, Miron, C., **Şipoş, Emilia**, Crasi, M., Laboratory Manual for Electronic Devices and Circuits. Electronic Devices, Editura U.T. Press, Cluj Napoca, ISBN 973-662-071-9, 112 pag, 2004.

e) Articole publicate în reviste din fluxul științific internațional principal

1. **Şipoş, Emilia**, Ciuciu, C., Ivanciu, Laura, Sensor-Based Prototype of a Smart Assistant for Visually Impaired People—Preliminary Results" Sensors 22, no. 11: 4271, 2022
<https://doi.org/10.3390/s22114271>
2. Oltean, G., Miron, C.; **Şipoş, Emilia**, Ivanciu, Laura, A top-down approach to gaining an insight into a transistor circuit in CS connection, International Journal of Electrical Engineering Education (IJEEE), Vol. 48, No.1, Jan.2011, ISSN 0020-7209, pp. 10-16
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DOI: [10.1109/TE.2005.858407](https://doi.org/10.1109/TE.2005.858407)
4. Oltean, G., Hintea, S., **Şipoş, Emilia**, A Genetic Algorithm-Based Multiobjective Optimization for Analog Circuit Design, Knowledge-based and intelligent information and engineering systems, PT II, Proceedings of 13th International Conference on Knowledge-Based Intelligent Information and Engineering Systems, Santiago, CHILE, Book Series Lecture Notes in Artificial Intelligence, Volume 5712, Pages 506-514, 2009
<https://www.webofscience.com/wos/woscc/full-record/WOS:000273459700063>
5. **Şipoş, Emilia**, Festila, Lelia, Oltean, G. Towards reconfigurable circuits based on ternary controlled analog multiplexers/demultiplexers, Knowledge-based intelligent information and engineering systems, PT 3, Proceedings of 12th International Conference on Knowledge-Based Intelligent Information and Engineering Systems, Zagreb, CROATIA, Book Series Lecture Notes in Artificial Intelligence, Volume 5179, Page 351-359, 2008
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f) Publicații apărute în lucrări ale principalelor conferințe internaționale de specialitate

1. Ivanciu, L.N, **Şipoş, Emilia**, Hybrid System for Dropout Risk Evaluation of First Year Students, 2nd International Conference on Applied Mathematics, Modeling and Simulation (AMMS), Beijing, PEOPLES R CHINA, AUG 26-27, 2018, DEStech Transactions on Computer Science and Engineering, Vol. 305, pp. 74-78
<https://www.webofscience.com/wos/woscc/full-record/WOS:000455297900013>
2. **Şipoş, Emilia**, Ivanciu, L.N, Failure Analysis and Prediction using Neural Networks in the Chip Manufacturing Process, 40th International Spring Seminar on Electronics Technology (ISSE), Sofia, BULGARIA, MAY 10-14, 2017
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<https://www.webofscience.com/wos/woscc/full-record/WOS:000419281500111>
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<https://www.webofscience.com/wos/woscc/full-record/WOS:000259080200073>
6. Oltean, G., Miron, C., **Mocean, Emilia**, Multiobjective Optimization Method for Analog Circuits Design based on Fuzzy Logic, Proceedings of the 9th IEEE International Conference on Electronics, Circuits and Systems - ICECS 2002, September 15-18, 2002, Dubrovnik, Croatia, ISBN 0-7803-7597-1, pag. 777-780
<https://www.webofscience.com/wos/woscc/full-record/WOS:000180927800191>
7. Oltean, G., **Şipoş, Emilia**, Simulink implementation of fuzzy functional models for analog modules, IEEE-TTTC International Conference on Automation, Quality and Testing, Robotics Location: Cluj Napoca, ROMANIA Date: MAY 25-28, 2006
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8. **Şipoş, Emilia**; Ivanciu, Laura, Fuzzy logic based fault detection and diagnosis: an automated design methodology. In: 2019 IEEE 25th International Symposium for Design and Technology in Electronic Packaging (SIITME). IEEE, 2019. p. 371-374.
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9. **Şipoş, Emilia**; Oneş, Alexandra, Groza, R., Failure detection on PCBs: an image processing based approach. In: 2019 IEEE 25th International Symposium for Design and Technology in Electronic Packaging (SIITME). IEEE, 2019. p. 375-378.
<https://www.webofscience.com/wos/woscc/full-record/WOS:000564733700081>
10. Ivanciu, Laura Nicoleta, **Şipoş, Emilia**, Transition Towards Education 4.0 in the Electronic devices and circuits lab, the 16th international scientific conference elearning and software for education, else 2020, bucharest, April 30- May 01, 2020, Volume 1, Pages: 513-520, Scopus
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11. **Şipoş, Emilia**, Ivanciu, Laura Nicoleta, SMARTBOARD IN E-TEACHING: PRELIMINARY RESULTS IN ELECTRONICS LABORATORIES, THE 16th INTERNATIONAL SCIENTIFIC CONFERENCE eLEARNING and SOFTWARE for EDUCATION, eELSE 2020, BUCHAREST, April 30- May 01, 2020, Volume 1, Pages: 505-512, Scopus
DOI: 10.12753/2066-026X-20-066
12. Ivanciu, Laura Nicoleta, **Şipoş, Emilia**, LEARNING BY DOING: CHALLENGES AND ACHIEVEMENTS DURING A YEAR OF ONLINE TEACHING, 17th International

13. Ciubancan, Cristina, Ivanciu, Laura, **Şipoş, Emilia**, Prototype music recommendation system – preliminary results in using music as therapy, 7th International Conference on Advancements of Medicine and Health Care through Technology (ONLINE CONFERENCE) – MediTech2020
DOI: 10.1007/978-3-030-93564-1_30
14. **Şipoş, Emilia**, Oneş, Alexandra, Ivanciu, Laura, PCB quality check: optical inspection using color mask and image thresholding, International Conference on Automation, Quality and Testing, Robotics, MAY 19-21, 2022 -AQTR 2022, Lucrare susținută, în curs de publicare

g) Alte lucrări și contribuții științifice

1. **Şipoş, Emilia**, Groza, R., Ivanciu, Laura, Power consumption and noise margin comparison between simple ternary inverter and binary inverter, Acta Technica Napocensis, ISSN 1221-6542, Vol.58, Iss2/2017, pp.13
2. Ciuciu, C., Bărbuță, D., Săsăujan, S., **Şipoş, Emilia**, Autonomous scale model car with ultrasonic sensors and Arduino board. Acta Technica Napocensis, ISSN 1221-6542, Vol.58, Iss.4/2017, pp. 6-11
3. Ivanciu, Laura-Nicoleta, **Şipoş, Emilia**, Fuzzy Logic based Expert System for Academic Staff Evaluation and Progress Monitoring”, 2nd International Conference on Computer, Mechatronics and Electronic Engineering (CMEE2017), Xiamen, China, 2017, pp. 312-319
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5. **Şipoş, Emilia**, Halmi, Teodora, Ivanciu, Laura, Matlab Application for Implementation of Automotive Audit Process VDA 6.3-2010, Acta Electrotehnica, Nr. 5/2016, ISSN 2344-5637, Vol.57, pp. 560-565, 2016
6. **Şipoş, Emilia**, Ivanciu, Laura-Nicoleta, Cotta, T., Easy-E-Home Schooling in Electronic Devices and Circuits, Journal Plus Education (2015): 35-41
7. Emilia Sipos, Luiza Buzdugan, Laura Ivanciu, AidISO-THE FIRST STEP FOR SMALL AND MEDIUM-SIZED COMPANIES IN BECOMING ISO 9001: 2008 CERTIFIED, Acta Technica Napocensis, Volumul 56, Numărul 1, pp. 7-12, 2015
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12. Oltean, G., **Şipoş, Emilia**, Simulink Models for Operational Transconductance Amplifiers Using Fuzzy Systems, Journal of Control Engineering and Applied Informatics, Vol 8, No 4, 2006, ISSN 1454-8658, pp. 44-51
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15. **Şipoş, Emilia**, Ilies, Marius, Festila, Lelia, Oltean, G, A Reviewing of the Multivalent Logic Applications. MIN, MAX Logic Gates Implementation, Proc. of the International Workshop Control and Information Technology IWCIT 2005, Ostrava, ISBN 80-248-0906-0, Czech Republic, 15-16 SEptember, 2005, pp. 223-227.
16. Oltean, G., **Şipoş, Emilia**, Functional Modeling of a Transconductance Amplifier using Fuzzy Systems, Proc. of the International Workshop Control and Information Technology IWCIT 2005, Ostrava, ISBN 80-248-0906-0, Czech Republic, 15-16 SEptember, 2005, pp. 135-138.
17. Oltean, G., **Şipoş, Emilia**, Csipkes, Doris, A Procedure to Model the Input-Output Behavior of Analog Modules, Proc. of the International Conference Interdisciplinarity in engineering, Inter-Ing 2005, Tg. Mures, ISBN 973-7794-41-9, Romania, 10-11 Nov., 2005, pp. 703-708
18. Csipkes, Doris, Oltean, G., **Şipoş, Emilia**, Translatarea modelelor de tranzistoare MOS de nivel 49 la nivel 7 pentru simulatorul PSpice, Raport tehnic RTH 05006, UTCN, oct 2005, Raport tehnic

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